

Electronic Acknowledgement Receipt

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Application Number:	10779904
International Application Number:	
Confirmation Number:	9608
Title of Invention:	Method and apparatus for defect analysis of semiconductor integrated circuit
First Named Inventor/Applicant Name:	Masahiro Ishida
Customer Number:	22511
Filer:	Thomas K. Scherer/Staci Harris
Filer Authorized By:	Thomas K. Scherer
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		02008071003_rtoa_20071126135125.pdf	119443	yes	5
			b7db791e894025219582b9025e152a27bd7b493c		

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Miscellaneous Incoming Letter	1	1
	Amendment - After Non-Final Rejection	2	2
	Applicant Arguments/Remarks Made in an Amendment	3	5

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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